

SESHADRI RAO GUDLAVALLERU ENGINEERING COLLEGE

(An Autonomous Institution with permanent Affiliation to JNTUK, Kakinada)
Seshadri Rao Knowledge Village, Gudlavalleru – 521 356, Krishna Dist., A.P.

Examination Section

III B.Tech - I Semester (R17) Regular Examinations, November 2025 (Readmitted Students)

TIME TABLE

4th November 2025

Timings: 10:00 a.m. to 1.00 p.m

DATE & DAY	ECE
18.11.2025 (Tuesday)	Linear Integrated Circuits Applications (EC2536)
20.11.2025 (Thursday)	Digital Communications (EC2537)
22.11.2025 (Saturday)	Antennas and Wave Propagation (EC2538)
24.11.2025 (Monday)	Principles of VLSI Design (EC2539)
26.11.2025 (Wednesday)	CAD for VLSI (EC2540)
29.11.2025 (Saturday)	Automotive Electronics (EC2543)

Note: Seating arrangement will be notified on the day of each examination half an hour before the commencement of the examination.


Controller of Examinations

 Copy to: HoD –ECE/CoE

Tables of Principal/Vice-Principal (Academics)/Vice Principal (Administration)


Principal